



**National Conference on Electronics, Communication and Computation-2020**  
**Online Virtual Conference**  
 5-6 September, 2020  
 NIT Jamshedpur, Jharkhand, India



**Theme of the conference: Role of Electronics and Communication in pandemic situation**

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 Director, NIT Jamshedpur

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 Professor, IIT Kharagpur

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**IMPORTANT DATES**

Paper Submission: 10-8-2020

**Extended deadline: 15-8-2020**

**Firm deadline: 20-8-2020**

Notification of Acceptance: 25-8-2020

**Notification of Acceptance: 31-8-2020**

Final Paper Submission: 2-9-2020

Registration Deadline: 2-9-2020

Website: [www.nceccnitjsr.in](http://www.nceccnitjsr.in)

**Contact:**

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**National Conference on Electronics, Communication and Computation (NCECC-2020)** will be held on 5-6 September 2020 at National Institute of Technology Jamshedpur. NIT Jamshedpur is an institute of National Importance and secured rank-79 in NIRF Ranking 2020. NCECC-2020 will provide a platform for scientists, researchers, academicians and professionals across the country for dissemination of new research ideas in various areas of electronics, communication and computation. This conference is being sponsored by TEQIP-3 (a Government of India Project assisted by World Bank).

In addition to the parallel online technical sessions, the conference program will include world-class keynotes, industry sessions and panels, by renowned experts.

In wake of pandemic situation, the conference will be conducted online on CISCO WebEx platform/Google Meet.

Authors are invited to submit their original unpublished papers, in all areas of electronic devices, communication systems, signal processing, embedded systems and computation engineering.

The several pioneer researchers including IEEE Fellow and industrialist persons will be present for delivering the future trend of research directions.

High-quality papers accepted, presented and recommended by TPC of NCECC-2020 will be published in reputed Journals.

**Submit a paper today to be part of the NCECC 2020 program!**

**Author Information**

Authors are invited to submit their original unpublished papers, in all areas of electronic devices, communication systems, signal processing, embedded systems and computation engineering included in the following Tracks:

- Track 1:** Electronic Devices
- Track 2:** Communication Systems
- Track 3:** Signal Processing
- Track 4:** Embedded Systems
- Track 5:** Computation Engineering

Plagiarism level should be less than 25%

Papers should be submitted in standard IEEE Conference

**Template available at**

<https://www.ieee.org/conferences/publishing/templates.html>

**Submission Link:**

<https://easychair.org/my/conference?conf=ncecc2020#>

**Registration Fee:**

Author registration fee (up to 3 papers): Rs. 1,000

**Keynote Speaker**



Prof. Ramjee Prasad  
 Fellow, IEEE



Prof. Saswat Chakrabarti  
 Professor, IIT Kharagpur



Prof. Satybrat Jit  
 Professor, IIT Varanasi



Prof. D K Singh  
 Director, BIT Sindri



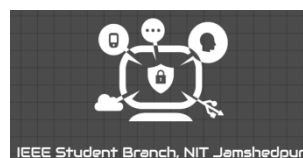
Prof. Vinod Kumar  
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## About NIT Jamshedpur

National Institute of Technology, Jamshedpur earlier known as Regional Institute of Technology (RIT) was established on 15th August 1960 as a joint venture of Government of India and the Government of Bihar in the chain of Regional Engineering Colleges in India with the aim to generate technical graduates of highest standards who could provide technological leadership to the region. On 27<sup>th</sup> December 2002, RIT was transformed to National Institute of Technology, Jamshedpur with the status of a Deemed University. The Institute has 12 departments including engineering, science and humanities. It has more than 200 faculty, 4000 students and 150 administrative and supporting staff. It has a self-contained campus located on the outskirts of Jamshedpur on 341.3 acres of sprawling, rolling hilly, wooded land. It combines natural beauty of countryside with urban glamour of a mineral-rich industrial area in the state of Jharkhand. It surrounded by giant and medium scale industries and reputed Institutes.

## About the Department

Department of Electronics and Communication Engineering was started in 1989. The department aims to be a national leader in imparting quality education, carrying out research and technology development. The department provides an outstanding research environment and offers academic program leading to the award of B.Tech, M Tech and Ph.D. degrees

## About TEQIP-3

TEQIP (Technical Education Quality Improvement Programme) is a World Bank assisted project implemented by MHRD to improve the quality of technical education in the country. NIT, Jamshedpur is included in the list of institutions to be funded. The support provided has facilitated improvements in upgradation & modernization of laboratories. The Project, Third phase of Technical Education Quality Improvement Programme (referred to as TEQIP-III) is fully integrated with the Twelfth Five-year Plan objectives for Technical Education as a key component for improving the quality of Engineering Education in existing institutions with a special consideration for Low Income States and Special Category States (SCS) and support to strengthen few affiliated technical universities to improve their policy, academic and management practices.

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## REGISTRATION FORM

**National Conference on Electronics,  
Communication and Computation  
NCECC-2020**

**September 5-6, 2020**

**Online Virtual Conference**

**Organized by**

**Department of Electronics and Communication Engineering**

**National Institute of Technology, Jamshedpur**

**Sponsored by**

**Technical Education Quality Improvement Programme  
(TEQIP-3)**

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**N.B.:** Photocopy of this form may also be used for registration. Scanned copy of the registration form should be sent by email to: ncecc2020@gmail.com.